

<b>IN THE UNITED STATES PATENT AND TRADEMARK OFFICE</b>	
In re application of: <b>Geetha Arthanari et al.</b>	Date: <b>04/17/2008</b>
Serial Number: <b>10/539,334</b>	Examiner: <b>Suchin Parihar</b>
Filed: <b>06/15/2005</b>	Group Art Unit: <b>2825</b>
Title: <b>ASIC CLOCK FLOOR PLANNING METHOD AND STRUCTURE</b>	IBM Corporation D/18G, B/321, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

**AMENDMENT UNDER RULE 312**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

OK TO ENTER: /SP/

05/09/2008

Sir:

/SP/

In accordance with 37 CFR 1.312, kindly amend the application as follows.